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#10/1-0-0-2873
Docket No. 0756-2344

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Shunpei YAMAZAKI et al.)
Serial No. 09/916,484)
Filed: July 30, 2001)
For: ELECTRIC DEVICE, MATRIX)
DEVICE, ELECTRO-OPTICAL DISPLAY)
DEVICE, AND SEMICONDUCTOR)
MEMORY HAVING THIN-FILM)
TRANSISTORS)

Art Unit: 2823
Examiner: S. Foong

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with
The United States Postal Service with sufficient postage as First
Class Mail in an envelope addressed to Commissioner for Patents,
Washington, D.C. 20231, on 7/19/02


SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231
Sir:

Further to the Information Disclosure Statement filed on July 30, 2001, Applicants note a typographical error in the Form PTO-1449. Specifically, JP 404,220,627 was incorrectly listed and should read as JP 4-220627. Submitted herewith is a Form PTO-1449 correctly listing JP 4-220627. It is respectfully requested that the Examiner line through the incorrect reference and initial the attached Form PTO-1449 evidencing consideration. A copy was submitted with the Response filed on July 15, 2002.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,


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